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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete If Known				
Application Number	110/039302			
Filing Date	January 2, 2002			
First Named Inventor	FANNING et al			
Art Unit	2112			
Examiner Name	MYERS, Paul R.			
Attorney Docket Number	42390P12354			

			U.S. PATE	NT DOCUMENTS	
Examiner Initials*	Cite No.'	Document Number Put	Publication Date	Name of Patentee or Pages, Columns, Lines, Wher Retevant Passages or Retevant Figures Appear	Pages Columns Lines Where
		Number - Kind Code² (if known)	or Issue Date MM-DD-YYYY		Relevant Passages or Relevant
PRM	1	US-6,330,639 B1	12-11-2001	Fanning et al.	
	2	US-2002/0069352 A1	06-06-2002	Fanning	
	3	US-6,467,031 B1	10-15-2002	Fanning	
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	12	US-6,625,695 B2	09-23-2003	Fanning	
	13	US-6,650,586 B1	11-18-2003	Fanning	
q.	14	US-6,684,311 B2	01-27-2004	Fanning	
PRM	15	US-6,701,469 B1	03-02-2004	Matter et al.	
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	Cite	Foreign Patent Document			Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Т
	No.'	Country Code ³ - Number ⁴ - Kind Code ⁸ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		T
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Examiner Signature	Paul R.	Marz	Date Considered	u/3/04	
				17101	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet 2 of 2

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	NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
Ppn	16	FANNING et al., "Method and Apparatus for Pripheral component Interface Bus Speed Selection", Abandoned Application # 09/467,106 - filed on 12/10/1999.	
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Examiner Signature	Paul R. Man	Date Considered 11/3/04	

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